Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	0	(((memory near2 test\$3) MBIST "memory built-in self-test") and ("random access memor\$3" RAM) and (\$4processor CPU tester ATE (test\$3 near controller)) and ("bus controller" ("utility bus" near controller)) and (bus "utility bus")). clm.	US-PGPUB	OR	ON	2006/08/04 11:27

Interference Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	0	(((memory near2 test\$3) MBIST "memory built-in self-test") and ("random access memor\$3" RAM) and (\$4processor CPU tester ATE (test\$3 near controller)) and ("bus controller" ("utility bus" near controller)) and (bus "utility bus")). clm.	US-PGPUB	OR	ON	2006/08/04 11:27
L3	0	(((memory near test\$3) MBIST "memory built-in self-test") and ("random access memor\$3" RAM) and (\$4processor CPU tester ATE (test\$3 near controller)) and ("bus controller" ("utility bus" near controller)) and (bus "utility bus")). clm.	US-PGPUB	OR	ON	2006/08/04 11:27
L2	8	(("random access memor\$3" RAM) and (\$4processor CPU tester ATE (test\$3 near controller)) and ("bus controller" ("utility bus" near controller)) and (bus "utility bus")). clm.	US-PGPUB	OR	ON	2006/08/04 11:26
L1	39	(("random access memor\$3" RAM) and (\$4processor CPU tester ATE (test\$3 near controller)) and ("bus controller" ("utility bus" near controller)) and (bus "utility bus")). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 11:25
S13 0	1	(((micro processor processor CPU "test controller") near3 access\$3 near3 memory) same (utility near2 bus))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 11:19
S13 1	0	"09940299"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 13:30
S12 9	0	(((micro processor processor CPU "test controller") near3 access\$3 near3 memory) same (utility near2 bus near2 controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 10:11

S12 6	429	(((micro processor processor CPU "test controller") near3 access\$3 near3 memory) same (bus near controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/03 10:09
S12 8	18	(711/104,154,100 365/100 714/718,719,736,42,724,1,5,30). ccls. and S126	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/01/06 14:09
S11 6	251	(711/104,154,100 365/100 714/718,719,736,42,724,1,5,30). ccls. and ((bus near2 (control\$3 manag\$5)) same ((disable control\$3) near4 test\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/01/06 14:08
S12 7	. 3	(((micro processor processor CPU "test controller") near3 access\$3 near3 memory) same (bus near controller)) and (test\$3 near2 memory).ti,ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 14:03
S12 5	7067	((micro processor processor CPU "test controller") near3 access\$3 near3 memory) same controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 14:02
S12 4	25333	((micro processor processor CPU "test controller") with access with memory) same controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 14:01
S12 3	0	09/940299	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 13:59
S12 2	19	(bus near3 arbitrat\$3) same (bus near control\$4) same (memory with test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 12:20
S12 1	800	(bus near3 arbitrat\$3) and (bus near control\$4) and (memory with test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 10:57

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S12 0	5318	(bus near3 arbitrat\$3) and (bus near control\$4) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 10:57
S11 9	3697	(bus near arbitrat\$3) and (bus near control\$4) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 10:56
S11 8	43990	(bus arbitrat\$3) and (bus near control\$4) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 10:56
S11 1	39	(((("Built-In Self-Test" "Self-Test") (test\$3 near (management execution engine))) same (array memory) same embedded) and (bus near control\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/06 10:55
S11 7	7	S116 and S111	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/01/05 13:53
S95	157	(711/104,154,100 365/100 714/718,719,42,724,5).ccls. and ((bus near2 (control\$3 manag\$5)) same ((disable control\$3) near4 test\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/01/05 13:51
S11 5	7	"Universal BIST Scheduler"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 13:44
S11 3	34	S111 not S107	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 13:44
S11 4	2	"4,608,669".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:53

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S11 2	34	S111 not S109	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:51
S10 9	5	((("Built-In Self-Test" "Self-Test") (test\$3 near (management execution engine))) same (array memory) same embedded same (bus near control\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:50
S11 0		S109 not S108	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:48
S10 8	4	((("Built-In Self-Test" "Self-Test") test\$3 near engine) same (Array memory) same Embedded same (bus near controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:45
S10 7	18	(("Built-In Self-Test" "Self-Test") same (Array memory) same Embedded same controller).ab,ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:41
S10 6	86	(("Built-In Self-Test" "Self-Test") same (Array memory) same Embedded same controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/05 12:34
S10 5	11	S104 and (memory near3 control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/05 10:36
S10 2	11	S101 and (memory near3 control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/05 10:36
S10 4	19	(utility near3 bus near2 control\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/05 10:35

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S10 1	28	(utility near3 bus near3 control\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/05 10:35
S10 3	3	(utility near bus near control\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/05 10:34
S10 0	0	(memory near3 (test or simulation) near3 (engine or apparatus or device or system)) and (utility near3 bus near3 control\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/24 12:19
S99	0	(memory near3 (test or simulation) near3 (engine or apparatus or device or system) and 714/42,718, 719,724.ccls.) and (utility near3 bus near3 control\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/24 12:19
S98	0	(memory near3 (test or simulation) near3 (engine or apparatus or device or system) and 714/42,718, 719,724.ccls.) and (utility near3 bus near3 control\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/24 12:19
S15	48	(memory with (test or simulation) with (engine or apparatus or device or system)and 714/719.ccls.) and bus adj3 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/24 12:17
S11	271	memory with (test or simulation) with (engine or apparatus or device or system)and 714/719.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/06/24 12:15
S97	3	"940299".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/09 12:43
S96	0	"09940299".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/09 12:43
S94	6	(711/104,154,100 365/100 714/718,719,42,724,5).ccls. and ((bus near4 (control\$3 manag\$5)) same (disable near4 test\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 17:28

Page 5

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593	0	((((memory with (test or simulation) with (engine or apparatus or device or system)) same (bus near4 control\$3) same (disable near4 test\$3)) and (711/104,154,100 365/100 714/718,719,42,724,5).ccls.))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 17:21
S92	0	((((memory with (test or simulation) with (engine or apparatus or device or system)) same (bus near4 control\$3) same (disable near4 test\$3)) and (711/104,154,100 365/100 714/718,719,42,724,5).ccls.))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 17:19
S91	7240	((((memory with (test or simulation) with (engine or apparatus or device or system)) same (bus near4 control\$3) same (disable near4 test\$3)) and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 17:17
S90	7240	((((memory with (test or simulation) with (engine or apparatus or device or system)) same bus near4 control\$3 same disable near4 test\$3) and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 17:13
S84	6930	((((memory with (test or simulation) with (engine or apparatus or device or system)) same bus near4 control\$3) and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 17:12
S83	6930	(((memory with (test or simulation) with (engine or apparatus or device or system)) same bus near4 control\$3) and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/05/21 13:10
S36	6630	"L12".ti,ab,bsum,clm,. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/05/21 13:08

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S82	49	(((memory with (test or simulation) with (engine or apparatus or device or system)) same bus near4 control\$3) and (714/718,719).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/21 11:54
S81	124	(memory with (test or simulation) with (engine or apparatus or device or system) and 714/718.ccls.) and bus near4 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/21 11:49
S18	82	(memory with (test or simulation) with (engine or apparatus or device or system)and 714/718.ccls.) and bus adj3 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/21 11:43
S80	19	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @ad <= "20010830" and data adj bus and burst	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/12/08 14:26
S79	5	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @ad <= "20010830" and block adj mode	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/12/08 14:25

S49	563	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @ad <= "20010830"	US-PGPUB; USPAT	OR	ON	2003/12/02 15:12
S78	0	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @ad <= "20010830" and asynchronous\$ near3 interrupt\$	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/12/02 14:47
S77	1960	"10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.) and asynchronous\$ near3 interrupt\$	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/12/02 14:45
S43	6659	"10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/12/02 14:44

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S76	1	(((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830" and asynchronous\$ near4 interrupt\$) and interrupt\$	US-PGPUB; USPAT; IBM_TDB	OR	ON	2003/12/02 14:40
S74	1	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830" and asynchronous\$ near4 interrupt\$	US-PGPUB; USPAT; IBM_TDB	OR	ON	2003/12/02 14:40
S75	1	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @ap <= "20010830" and asynchronous\$ near4 interrupt\$	US-PGPUB; USPAT; IBM_TDB	OR	ON	2003/12/02 14:38

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S71	60	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830" and interrupt\$	US-PGPUB; USPAT	OR	ON	2003/12/02 14:36
S72	12	(((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830" and asynchronous\$) and interrupt\$	US-PGPUB; USPAT	OR	ON	2003/12/02 14:34
S73	0	(("10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.) not @ad <= "20010830" and asynchronous\$ and interrupt\$	US-PGPUB; USPAT; IBM_TDB	OR	ON	2003/12/02 14:01
S46	46	(("10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.) not @ad <= "20010830"	US-PGPUB; USPAT	OR	ON	2003/12/02 14:01

S70	12	((((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830" and interrupt\$) and asynchron\$	US-PGPUB; USPAT	OR	ON	2003/12/01 15:24
S67	38	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3)) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830" and asynchronous\$	US-PGPUB; USPAT	OR	ON	2003/12/01 15:23
S69	0	asynchronous\$ and "6501690".pn.	USPAT	OR	OFF	2003/12/01 14:00
S68	0	asynchronous\$ and 6501690B2.pn.	USPAT	OR	OFF	2003/12/01 14:00
S50	433	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and @pd <= "20010830"	US-PGPUB; USPAT	OR	ON	2003/12/01 13:52
S66	1	"6370661".PN.	USPAT	OR	OFF	2003/12/01 09:40

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S65	4	SBRIC and @ad <= "20010830"	US-PGPUB; USPAT	OR	ON	2003/11/26 12:25
S64	0	((((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer).ti,ab, bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and (RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3).ti.) and SBRIC and @ad <= "20010830"	US-PGPUB; USPAT	OR	ON	2003/11/26 12:25
S63	1	"5819025".PN.	USPAT	OR	OFF	2003/11/25 16:34
S62	1	"5961653".PN.	USPAT	OR	OFF	2003/11/25 16:32
S61	1	"6003142".PN.	USPAT	OR	OFF	2003/11/25 16:31
S60	1	"6370661".PN.	USPAT	OR	OFF	2003/11/25 16:31
S59	0	system and method adj to adj asynchronously adj tests adj rams. ti.	US-PGPUB; USPAT	OR	ON	2003/11/25 15:29
S58	0	system and method adj to adj asynchronously adj tests adj rams. ti.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:29
S57	0	"system adj and adj method adj to adj asynchronously adj tests adj rams".ti.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:28
S56	1507935	system and method to asynchronously tests rams.ti.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:27
S55	0	"09940299".ap.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:26
S54	3	"940299".ap.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:23
S53	2	Platt-alfred\$.in.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:22
S52	0	Kwong-simon\$.in.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:20
S51	80	Wendorf\$.in.	US-PGPUB; USPAT	OR	OFF	2003/11/25 15:20
S24	107296	(RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/21 10:00

S47	563	(("10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or	US-PGPUB; USPAT	OR	ON	2003/11/21 09:58
		714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.) and @ad <= "20010830"	,			
S48	1	"6370661".PN.	USPAT	OR	OFF	2003/11/20 15:42
S45	609	("10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.	US-PGPUB; USPAT	OR	ON	2003/11/20 13:50
S44	963	("10".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:38
S40	6680	"12".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100). ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:36
S42	612	("12".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.	US-PGPUB; USPAT	OR	ON	2003/11/20 12:24
S41	966	("12".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)) and "4".ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:09
S39	5116	"4".ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:03
S38	5	"L4".ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:03
S37	6630	"L12".ti,ab,bsum,clm. and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:03

S35	6710	(((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) with ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3))) and ((711/104 or 711/154 or 711/100 or 365/100).ccls.) or ((714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 12:00
S34	6630	(714/718 or 714/719 or 714/42 714/724 or 714/5).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 11:58
S33	2510	(711/104 or 711/154 or 711/100 or 365/100).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 11:55
S32	15769	((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) with ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 11:07
S31	18332	(RAM or storage or memor\$3) with ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:29
S30	12843	((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3)) same ((test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)) same (control\$4 processor microprocessor cpu computer microcomputer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:28
S29	182723	(test\$3 diagnos\$4 analy\$6 simulat\$3) near8 (engine operation software instruct\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:22
S28	4308277	control\$4 processor microprocessor cpu computer microcomputer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:19
S27	46281	(test\$3 diagnos\$4 analy\$6 simulat\$3) near8 ((RAM or storage or memor\$3) near12 (test\$3 diagnos\$4 analy\$6 simulat\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:17
S26	3576446	engine operation software instruct\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:16

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S25	1650225	test\$3 diagnos\$4 analy\$6 simulat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:15
S23	1847885	RAM or storage or memor\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/20 10:11
S22	192	(memory with (test or simulation) with (engine or apparatus or device or system)) same bus adj3 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/20 09:21
S21	1	"6247153".PN.	USPAT	OR	OFF	2003/11/19 14:59
S20	1	"6088823".PN.	USPAT	OR	OFF	2003/11/19 14:58
S19	1	"6263461".PN.	USPAT	OR	OFF	2003/11/19 14:55
S17	24	(memory with (test or simulation) with (engine or apparatus or device or system)and 714/42.ccls.) and bus adj3 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:13
S16	49	(memory with (test or simulation) with (engine or apparatus or device or system)and 714/724.ccls.) and bus adj3 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:13
S14	0	(memory with (test or simulation) with (engine or apparatus or device or system)and 714/718.ccls.) and (memory with (test or simulation) with (engine or apparatus or device or system)and 714/719.ccls.) and (memory with (test or simulation) with (engine or apparatus or device or system)and 714/724.ccls.) and (memory with (test or simulation) with (engine or apparatus or device or system)and 714/42.ccls.) and bus adj3 control\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:12
S13	132	memory with (test or simulation) with (engine or apparatus or device or system)and 714/42.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:09
S12	305	memory with (test or simulation) with (engine or apparatus or device or system)and 714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:08
S10	800	memory with (test or simulation) with (engine or apparatus or device or system)and 714/718.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:07

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S9	0	memory with (test or simulation) with (engine or apparatus or device or system)and 714/718.ccls. and 714/719.ccls. and 714/42.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:07
S8	13399	memory with (test or simulation) with (engine or apparatus or device or system)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:05
S2	2532	714/718.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 14:03
S7	0	714/719.ccls. and "20010901".fd.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 11:33
S6	683	714/719.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 11:32
S5	2765	714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 11:30
S4	0	714/324.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 11:30
S3	433	714/42.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/19 11:22
S1	50	("5961653" "5963249" "4373180" RE31864 "5331643" "5469443" "5528553" "55555249" "5615335" "5617534" "5640509" "5872018" "5937367" "5977558" "6000048" "6046946" "4333142" "4429368" "4450560" "4827476" "4833677" "4873705" "4947167" "4965799" "5351213" "5416919" "5425035" "5463585" "5513318" "5537632" "5553238" "5577230" "5579477" "5588046" "5590273" "5592616" "5706234" "5729679" "5831987" "5852581" "5875135" "5898629" "5913020" "5951703" "5996097" "6009536" "6018808" "6046955" "6097644" "6115763").pn.	USPAT	OR	OFF	2003/11/18 15:30